

N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	Rds(on) max	I _{D MAX} T _C = +25°C	
700V	1.5Ω @ V _{GS} = 10V	5.0A	

Features and Benefits

- Low On-Resistance
- High BV_{DSS} Rating for Power Application
- Low Input Capacitance
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

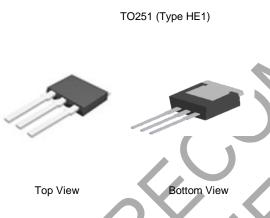
Description and Applications

This MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$) and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

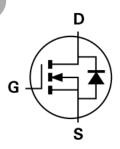
- Adaptor
- LCD & PDP TV
- Lighting

Mechanical Data

- Case: TO251 (Type HE1)
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 <a>3
- Weight: 0.33 grams (Approximate)







Top View Pin Configuration

Internal Schematic

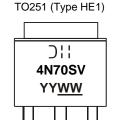
Ordering Information (Note 4)

Part Number	Case	Packaging
DMJ70H1D5SV3	TO251 (Type HE1)	75pieces / Tube

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- See http://www.diodes.com/quality/lead_free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



☐ I = Manufacturer's Marking 4N70SV = Product Type Marking Code YYWW = Date Code Marking YY or <u>YY</u>= Last Two Digits of Year (ex: 18 = 2018) WW or <u>WW</u> = Week Code (01 to 53)



NOT RECOMMENDED FOR NEW DESIGN -NO ALTERNATE PART

DMJ70H1D5SV3

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage		V_{DSS}	700	V
Gate-Source Voltage		V _{GSS}	±30	V
Continuous Drain Current (Note 5) V _{GS} = 10V	$T_C = +25$ °C $T_C = +100$ °C	I _D	5.0 3.2	А
Maximum Body Diode Forward Current (Note 6)	·	I _S	3.0	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I _{DM}	6.0	Α
Avalanche Current (Note 7)	L = 60mH	I _{AS}	0.5	Α
Avalanche Energy (Note 7)	L = 60mH	Eas	7.5	mJ
Peak Diode Recovery dv/dt (Note 7)		dv/dt	5.2	V/ns

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	$T_C = +25^{\circ}C$ $T_C = +100^{\circ}C$	P _D	78 31	W
Thermal Resistance, Junction to Ambient (Note 6)		RθJA	80	°C/W
Thermal Resistance, Junction to Case (Note 5)		R _{0JC}	1.8	C/VV
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

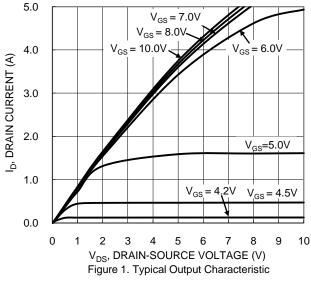
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

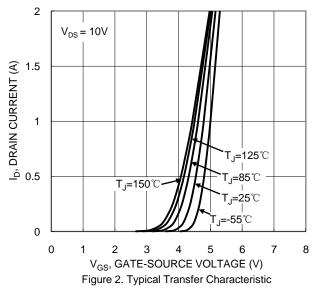
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	700		-	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	IDSS			1	μA	$V_{DS} = 700V, V_{GS} = 0V$	
Gate-Source Leakage	lgss	7	_	100	nA	$V_{GS} = \pm 30V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)	ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(TH)}	2	3.4	4	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance	R _{DS(ON)}	1	1.25	1.5	Ω	$V_{GS} = 10V$, $I_D = 1A$	
Diode Forward Voltage	V _{SD}	/	0.85	1.3	V	$V_{GS} = 0V, I_{S} = 1A$	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	C _{iss}	_	316	_		V _{DS} = 50V, f = 1MHz, V _{GS} = 0V	
Output Capacitance	Coss	_	124	_	pF		
Reverse Transfer Capacitance	Crss	_	3.9	_			
Gate Resistance	Rg	_	2.2	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge	▶ Q _g	_	9.8	_		$V_{DD} = 560V, I_D = 3.2A,$ $V_{GS} = 10V$	
Gate-Source Charge	Qgs	_	1.3	_	nC		
Gate-Drain Charge	Q_{gd}	_	5.3	_		VGS = 10V	
Turn-On Delay Time	t _{D(ON)}	_	7.5	_			
Turn-On Rise Time	t _R	_	10	_	ns	$V_{DD} = 350V, V_{GS} = 10V,$ $R_g = 4.7\Omega, I_D = 3.2A$	
Turn-Off Delay Time	t _{D(OFF)}	_	21	_	115		
Turn-Off Fall Time	t _F	_	5	_			
Body Diode Reverse Recovery Time	t _{RR}	_	190	_	ns		
Body Diode Reverse Recovery Time (T _J = +150°C)	t _{RR}	_	255	_	ns	1 2 2 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4	
Body Diode Reverse Recovery Charge	Q_{RR}	_	1.4	_	μC	$I_S = 3.2A$, $dI/dt = 100A/\mu s$	
Body Diode Reverse Recovery Charge (T _J = +150°C)	Q _{RR}	_	2.0	_	μC	1	

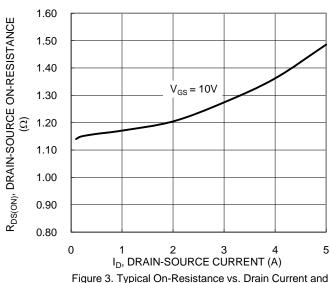
Notes:

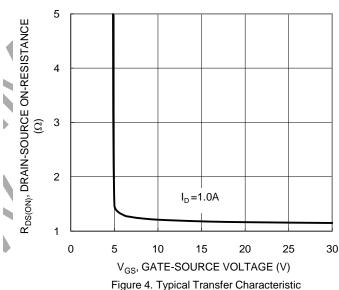
- Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout
 Device mounted on FR-4 substrate PC board, 2oz. copper, with minimum recommended pad layout.
- 7. Guaranteed by design. Not subject to production testing.8. Short duration pulse test used to minimize self-heating effect.

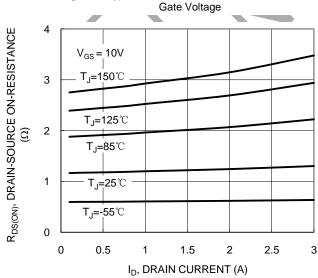












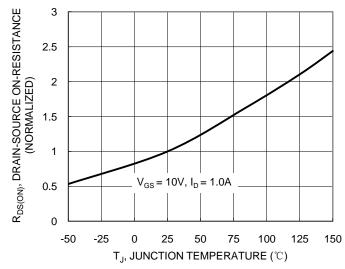


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

Figure 6. On-Resistance Variation with Temperature

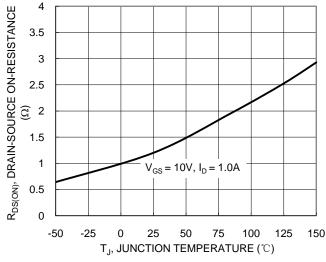
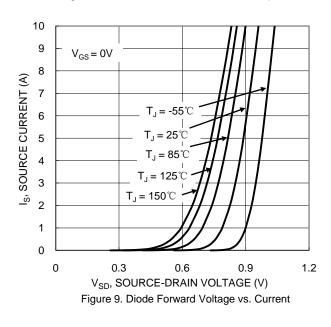
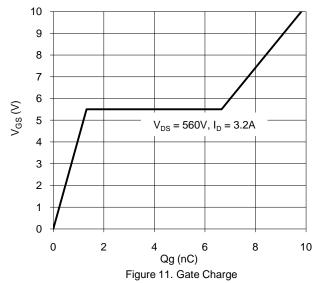


Figure 7. On-Resistance Variation with Temperature





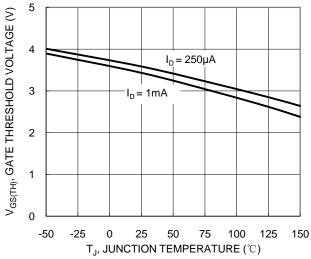


Figure 8. Gate Threshold Variation vs. Junction Temperature

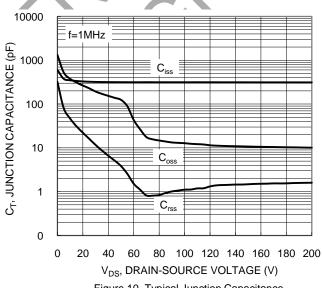
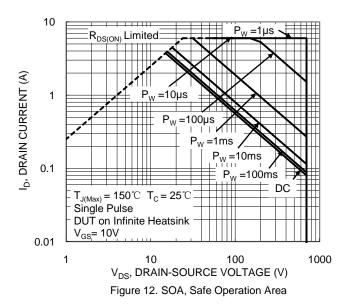
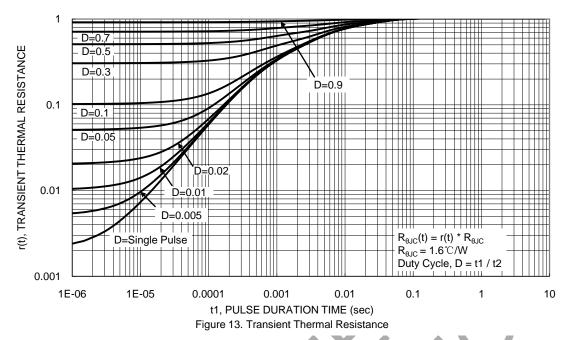


Figure 10. Typical Junction Capacitance



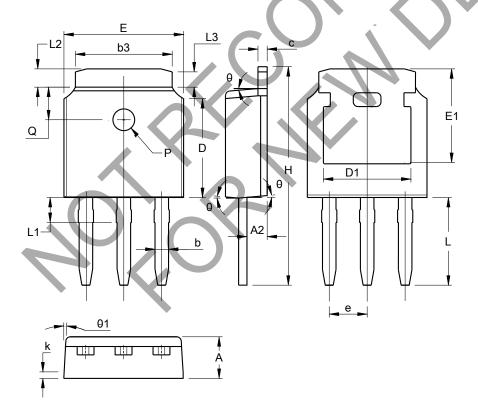




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

TO251 (Type HE1)



TO251 (Type HE1)						
Dim	Min	Max	Тур			
Α	2.20	2.40	2.30			
A2	0.97	1.17	1.07			
b	0.68	0.90	0.78			
b3	5.20	5.50	5.33			
С	0.43	0.63	0.53			
D	5.98	6.22	6.10			
D1	5	.30 RE	F			
е	2.	286 BS	C			
Е	6.40	6.80	6.60			
E1	4.63	5.03	4.83			
Н	10.00	11.44	11.22			
k	C).40REI				
L	3.90	4.30	4.10			
L1	0.85	1.25	1.05			
L2	0.88	1.28	1.02			
L3	0.75 REF					
Q	1.65	1.95	1.80			
PØ	1.20					
θ	5°	9°	7°			
θ1	5°	9°	7°			
All Dimensions in mm						



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DMJ70H1D5SV3

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